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Substitute for form 1449A/PTO				Application Number		Filed Herewith- <u>10/003602</u>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Filing Date		11/14/2001	
				First Named Inventor:		Wingyu Leung	
				Examiner name: unknown		GROUP: unknown	
Sheet 1 of 1				Attorney Docket Number		MST-012	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cited No. ¹	U.S. Patent Document Number	Kind Code ² (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear
CWS	A1	5003542		Mashiko et al.	03/26/1991	
↓	A2	5638385		Fifield et al.	06/10/1997	
↓	A3	6065146		Bosshart	05/16/2000	

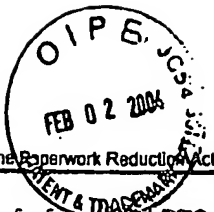
FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cited No. ¹	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
CWS	"16-Bit CMOS Error Detection And Correction Unit", ©1990 Integrated Device Technology, Inc., pages 1-19
↓	"Cost Analysis of On Chip Error Control Coding for Fault Tolerant Dynamic RAMs", by Najmi Jarwala and Dhiraj K. Pradham, 1987 IEEE, pages 278-283
↓	"A Class of Optimal Minimum Odd-weight-column SEC-DED Codes" by M.Y. Hsiao, July 1970, pages 395-401

EXAMINER <u>Christina</u>	DATE CONSIDERED <u>8/6/00</u>
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PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449 A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/003,602
		Filing Date	11/14/2001
		First Named Inventor:	Wingyu Leung
		Examiner name: A. Decady	GROUP: 2133
Sheet	1 of 1	Attorney Docket Number	MST-012

U.S. PATENT DOCUMENTS

Examiner Initials*	Cited No. ¹	Document Number Number - Kind Code ^{2(f)(known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear
CJS	B1	US-5,524,220	06-04-1996	Verma et al.	
↓	B2	US-6,147,535	11-14-2000	Leung	
		US-			
		US-			
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Cntry Code ³ Number ⁴ Kind Code ^{5(f)(known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T ⁶
CJS	B3	GB 2289779	11-29-1995	Intel Corporation		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

CJS	"Error detection and correction with the IDT49C466" by Anupama Hegde, 12/18/1994, No. 10, pages 613-620.
CJS	"Error Correction Circuitry for Dynamic Random Access Memories", IBM Technical Disclosure Bulletin, Vol. 36, No. 08, August 1993, pages 681-682.

EXAMINER SIGNATURE: *Christina*

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